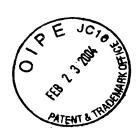
## Method and Circuitry for Active Inrush Current Limiter and Power Factor Control S/N 09/749,354



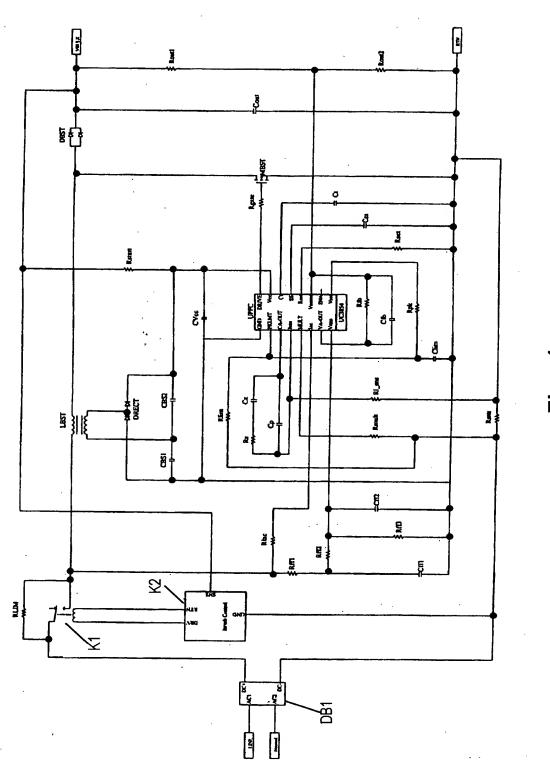
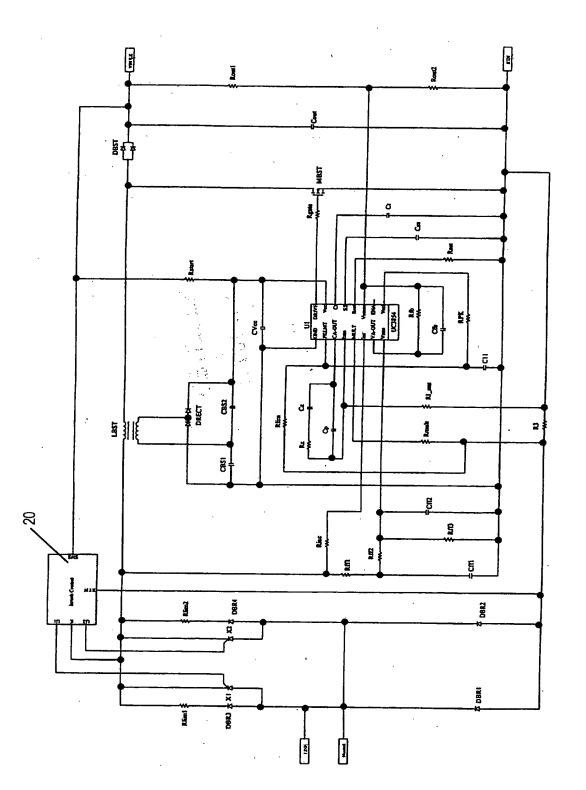


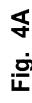
Fig. , Prior A

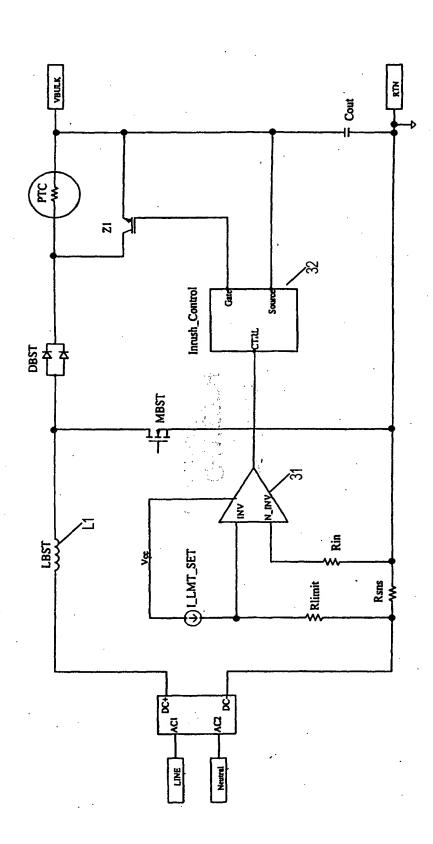














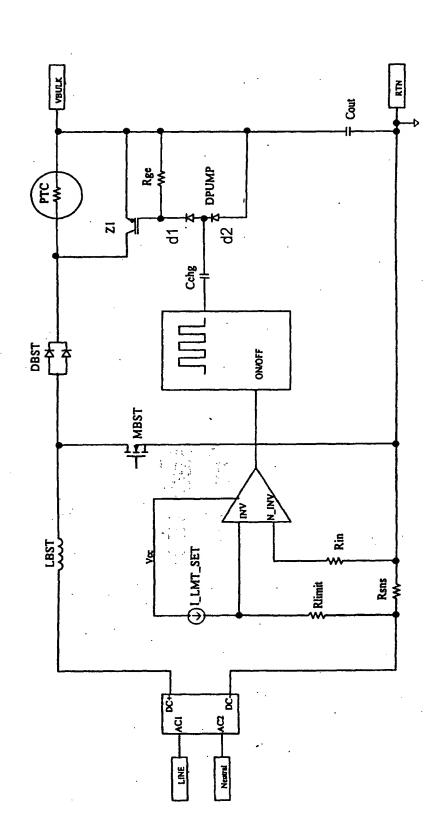
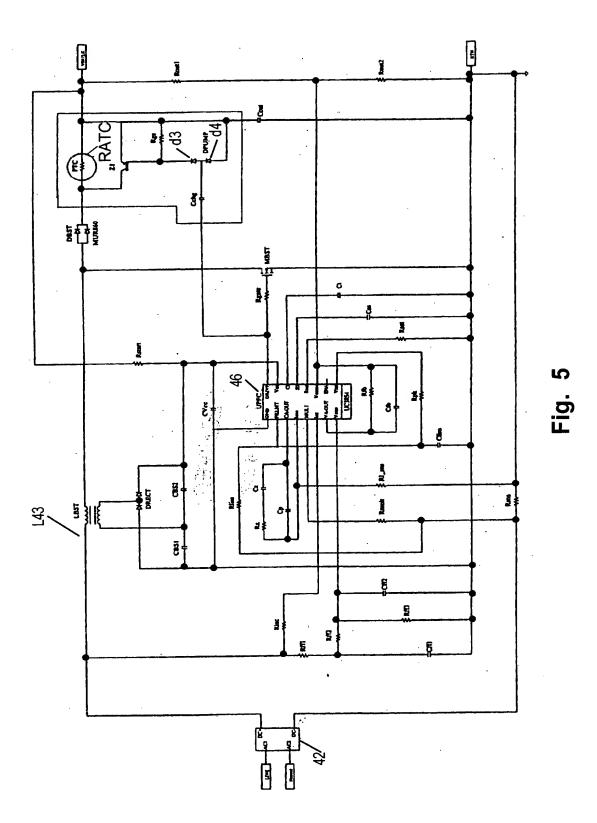
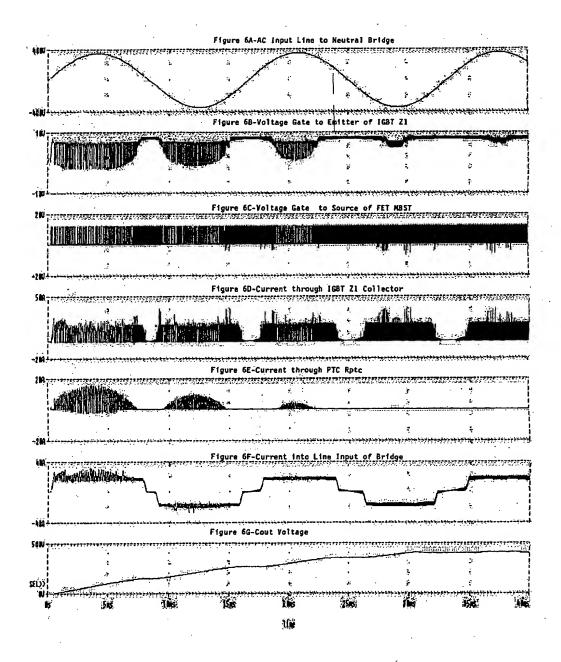


Fig. 4B



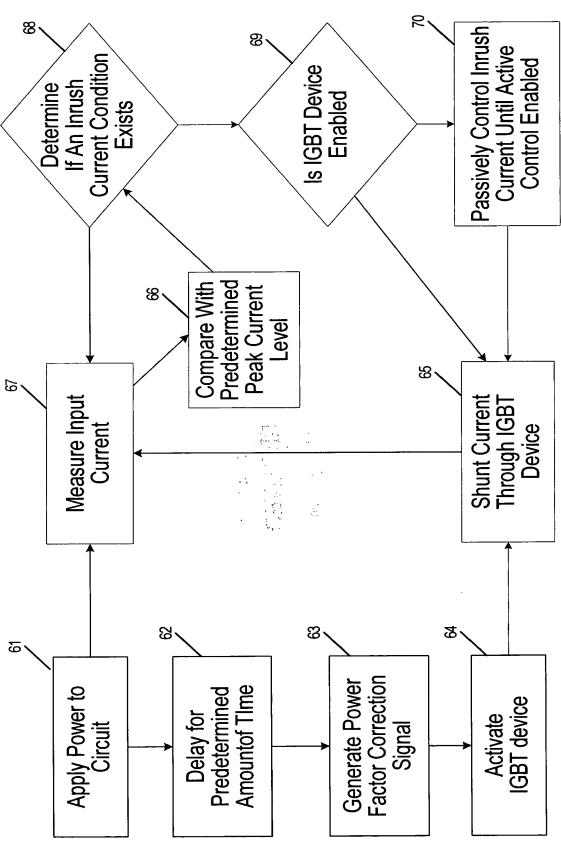






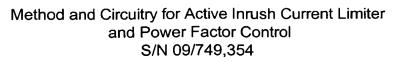
**Fig. 6** 7/10

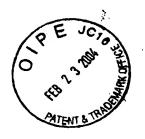




810

Fig. 7





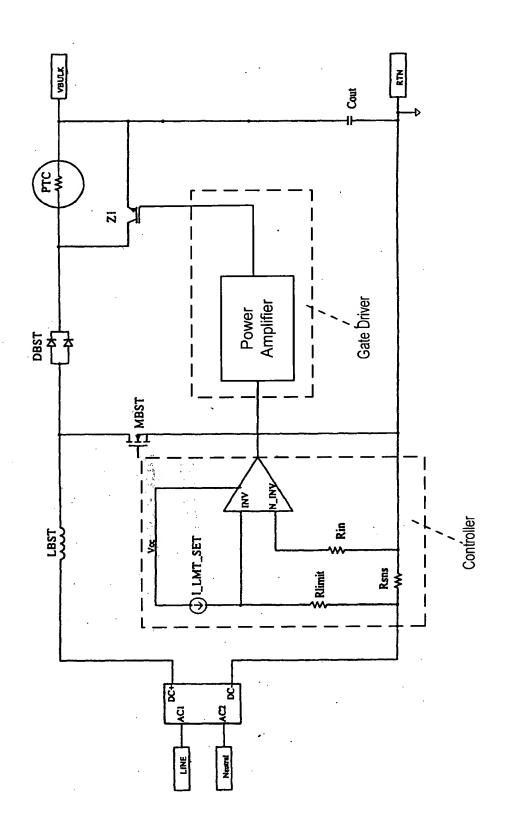
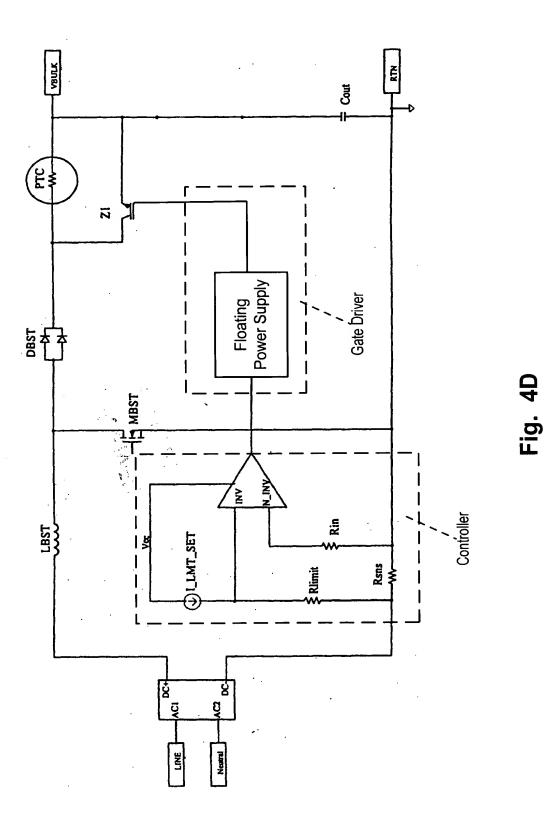


Fig. 40







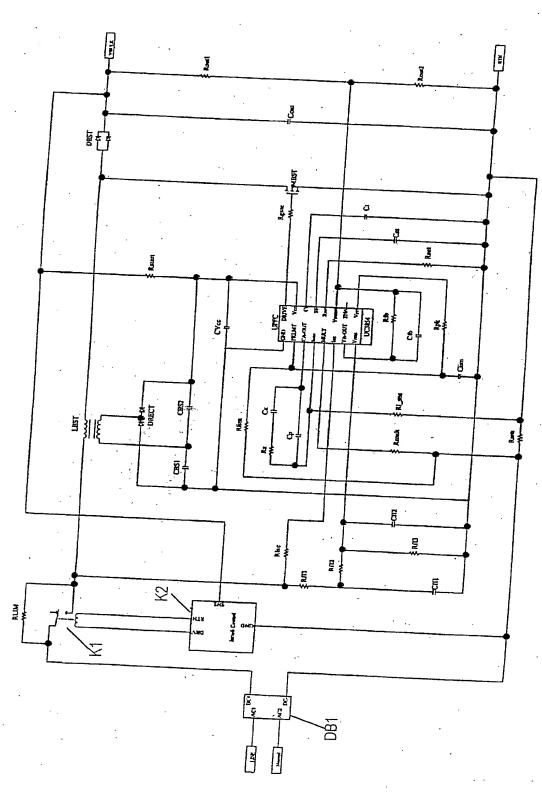


Fig. 1 Prior Art



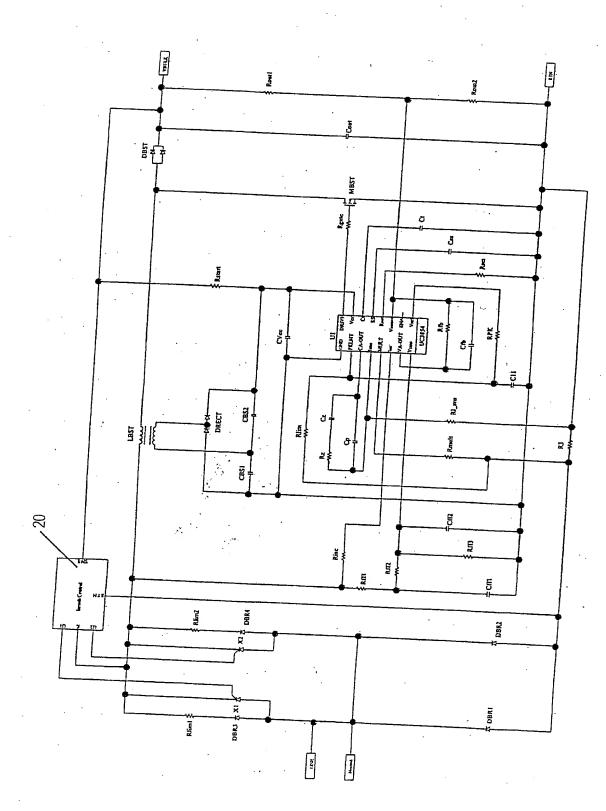
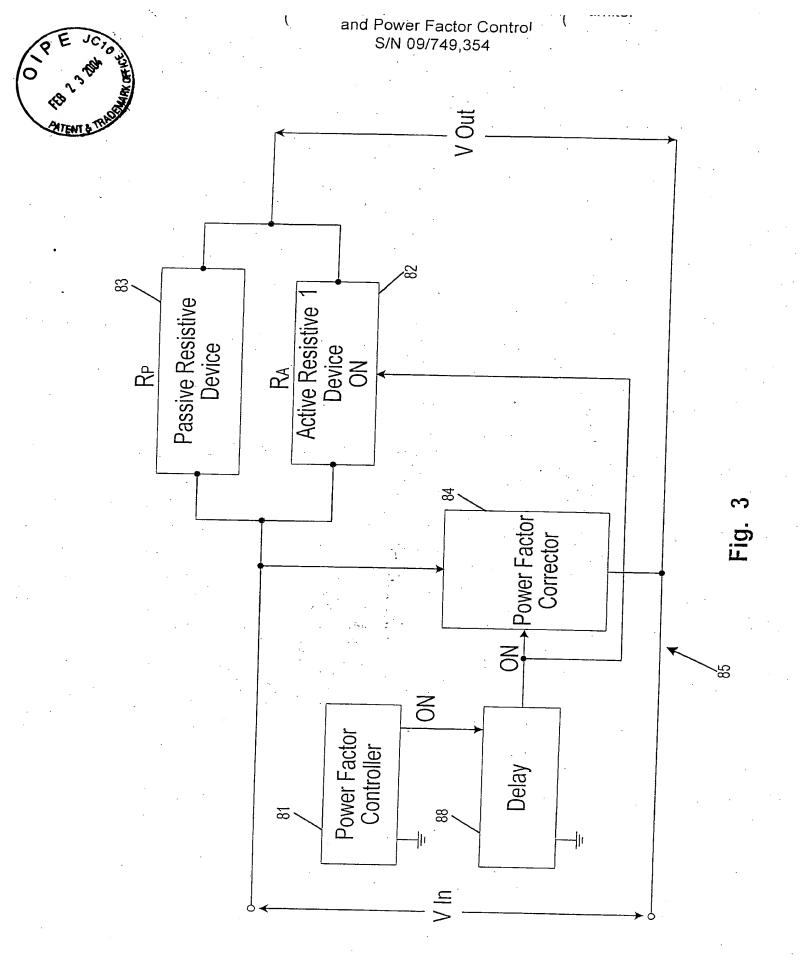
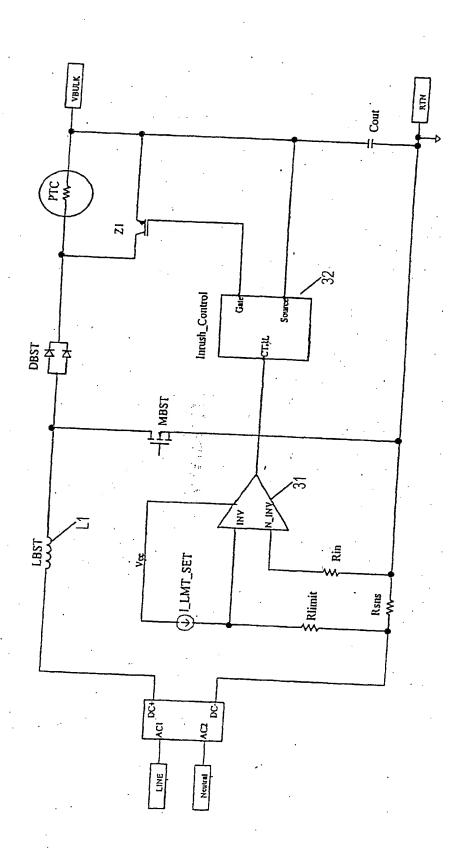


Fig. 2 Prior Art









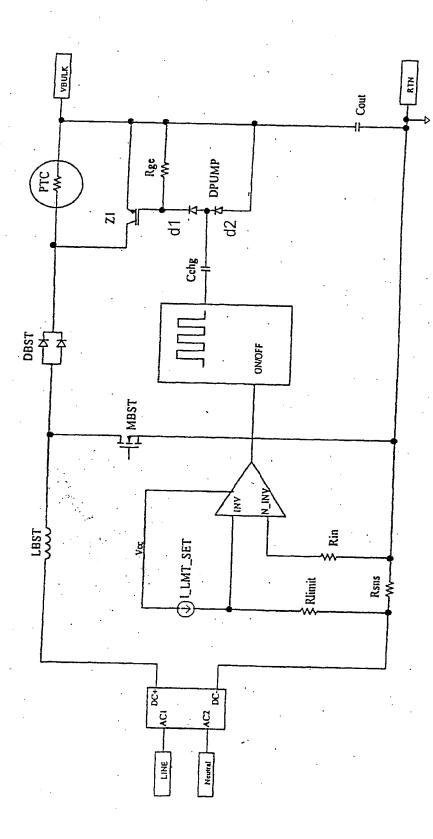
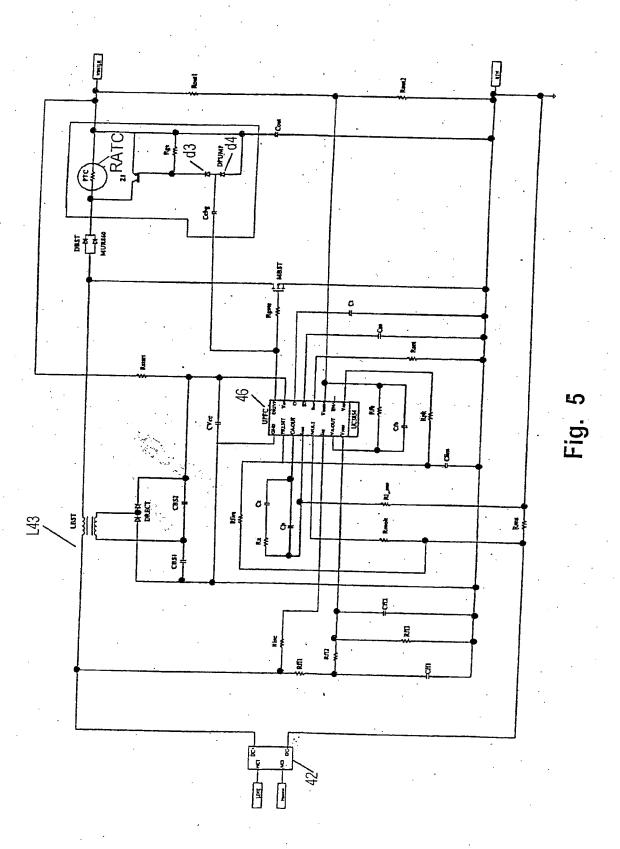


Fig. 4B









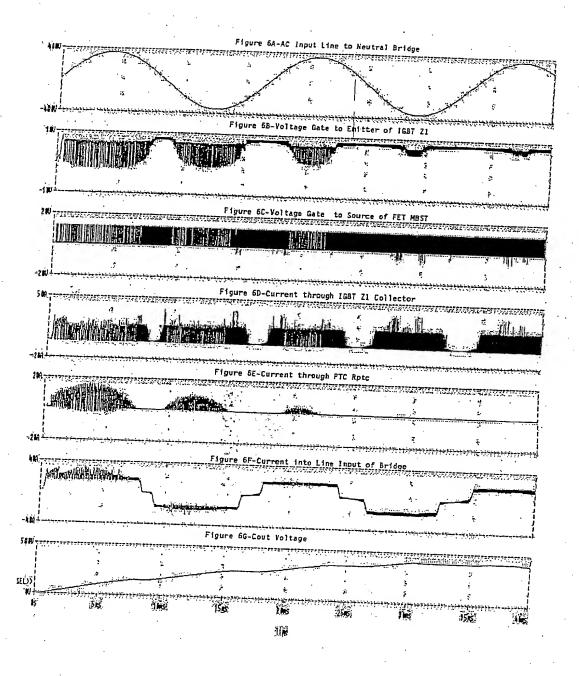


Fig. 6

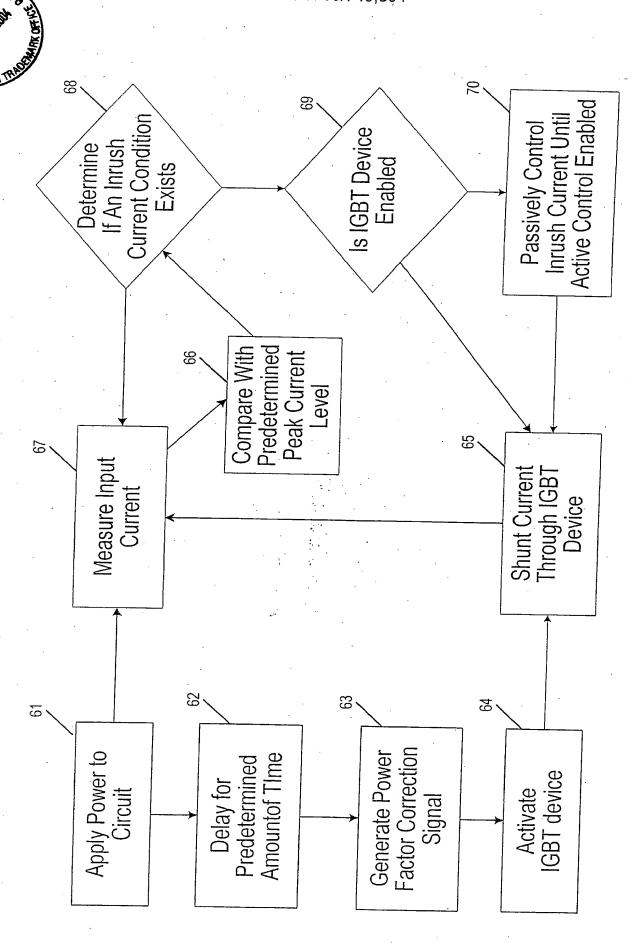


Fig. 7